

**What is claimed is:**

1. A surface mount composite electronic component in which a circuit element(s) is(are) formed on a pair of opposed surfaces of an insulating substrate composed of a hexahedron, comprising electrodes that make up the circuit element which also function as external terminals.

2. A surface mount composite electronic component in which each circuit element is formed on each surface of a set of opposing surfaces of an insulating substrate composed of a hexahedron, comprising electrodes that make up the circuit elements which also function as external terminals.

3. A surface mount composite electronic component in which circuit elements are formed on each surface of a set of opposing surfaces of an insulating substrate composed of a hexahedron, wherein the circuit element is arranged in a direction parallel to a mount surface through electrodes that make up the circuit element and which also function as external terminals.

4. A surface mount composite electronic component comprising:  
a pair of first electrodes disposed on both ends of a front surface of an insulating substrate composed of a hexahedron;  
a pair of second electrodes disposed on a rear surface of the insulating substrate opposite the first electrodes;  
a first resistor disposed so as to contact both of the first pair of electrodes; and  
a second resistor disposed so as to contact both of the second electrodes.

5. A surface mount composite electronic component comprising:

a pair of first electrodes disposed on both ends of a front surface of an insulating substrate composed of a hexahedron and a first shared electrode disposed between and separate from said first electrodes;

a pair of second electrodes disposed on a rear surface of the insulating substrate opposite the first electrodes and a second shared electrode disposed between and separate from said second electrodes;

two first resistors disposed so as to contact the first pair of electrodes and the first shared electrode; and

two second resistors disposed so as to contact the second pair of electrodes and the second shared electrode.

6. The surface mount composite electronic component according to claim 5, wherein the number of external terminals is six.

7. The surface mount composite electronic component according to any one of claims 1 through 6, wherein the circuit element is composed of electrodes as well as resistors or dielectrics that contact said electrodes, a nickel plate layer and a solder plate layer laid down, in that order, on the surfaces of external terminal areas of said electrodes.

8. The surface mount composite electronic component according to any one of claims 1 through 7, wherein one surface of the insulating substrate is substantially perpendicular to an adjacent surface.

9. The surface mount composite electronic component according to any one of claims 1 through 8, having an area in which no electrodes are present on an edge portion of the surface of the insulating substrate on which said external terminals are present, disposed perpendicular to a straight line connecting a pair of external terminals electrically connected to one circuit element.

10. The surface mount composite electronic component according to any one of claims 1 through 9, having an area in which the electrodes and the resistor overlap and contact each other on the surface of the insulating substrate, the resistor disposed on the insulating substrate in said area, and further, the electrodes laid on the resistor.

11. The surface mount composite electronic component according to any one of claims 1 through 10, wherein a relation between an insulating substrate dimension (L) in a direction of flow of electric current of the circuit element of the insulating substrate surface on which circuit elements are formed, an insulating substrate dimension (T) perpendicular to L, and a distance (W) between surfaces of the insulating substrate on which the circuit elements are formed is  $L \geq W > T$ .

12. The surface mount composite electronic component according to any one of claims 1 through 11, wherein a surface of the insulating substrate facing a circuit board is smooth.

13. The surface mount composite electronic component according to any one of claims 1 through 12, wherein the electrodes that also function as external terminals are also present on a surface of the insulating substrate adjacent to the surface of the insulating substrate on which the circuit elements are present.

14. The surface mount composite electronic component according to any one of claims 1 through 13, wherein one or all of the insulating substrate, the electrodes and the resistors are made mainly of a resin-type material.

15. The surface mount composite electronic component according to any one of claims 1 through 14, wherein a long side of the insulating substrate on which circuit elements are formed is 1.0 mm or less.

16. A method of manufacturing a surface mount composite electronic component, comprising:

a step of forming predetermined circuit elements on both side surfaces of an insulating substrate of divisional units of a large-scale insulating substrate demarcated latitudinally and longitudinally on both side surfaces thereof at opposing locations; and

a step of dividing the large-scale insulating substrate into said divisional units,

a composite electronic component composed of circuit elements on both side surfaces of the insulating substrate being formed by these steps,

one of the surfaces of the insulating substrate other than the both side surfaces of the insulating substrate being a surface facing a circuit board.

17. The surface mount composite electronic component according to claim 15, wherein formation of the insulating substrate surface facing the circuit board is accomplished by dicing.